	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	475777	semiconductor and (tft or (thin film transistor))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2001/09/29 14:10
2	BRS	L2	6974	1 and gate and drain and source and offset	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2001/09/29 14:12
3	BRS	L3	910	2 and sidewall	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2001/09/29 14:12
4	BRS	L4	910	3 and (gate electrode)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2001/09/29 14:13
5	BRS	L5	910	4 and (offset region)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2001/09/29 14:13

	Comments	Error Definition	Errors
1			0
2			0
3			0
4			0
5			0
	Comments	Error Definition	Errors

	Type	L #	Hits	Search Text	DBs	Time Stamp
6	BRS	L6	802	5 and (thin film)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2001/09/29 14:15

	Comments	Error Definition	Err ors
6			0